



# TECHNé

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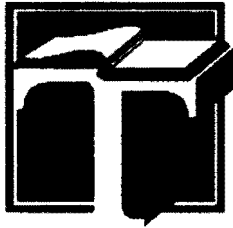
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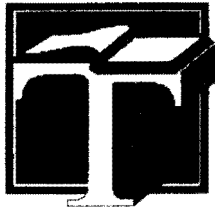
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***HEART RATE COMPUTATION IMPLEMENTED ON FIELD  
PROGRAMMABLE GATE ARRAY BOARD***

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**Abstract**

Heart rate is the most basic information of heart activities. This paper aims to describe a possible real time system to detect electrocardiogram (ECG) signal, count the R-R interval of the ECG signal, and compute the heart rate. This project is implemented successfully using field programmable gate array (FPGA) board.

**Keywords:** Heart rate, ECG, FPGA.

